

Form PTO-1449 (Modified)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

Atty. Docket No.
2085-00700Serial No.
10/052,117Applicants
Ying-Chang LIANG et al.Filing Date
January 17, 2002Group
2661 2616

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	Translation YES NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

DH	AA	Ghuri and D. Slock, "Linear Receivers For DS-CDMA Downlink Exploiting Orthogonality Of Spreading Sequences", Proc. of Asilomar Conf. On Signals, Systems and Computers, pp. 650-654, November 1998.
DH	AB	K. Hooli, M. Latva-aho and M. Juntti, "Multiple Access Interference Suppression With Linear Chip Equalization In WCDMA Downlink Receivers", Proc. of Globecom'99, pp. 467-471, December 1999.
DH	AC	M. Heikkila, P. Komulainen and J. Lilleberg, "Interference Suppression In CDMA Downlink Through Adaptive Channel Equalization", Proc. of VTC'Fall-99, pp. 978-982, September 1999.
DH	AD	K. Li and H. Liu, "A New Blind Receiver For Downlink DS-CDMA Communications", IEEE Communications Letters, Vol. 3, No. 7, pp. 193-195, July 1999.
DH	AE	F. Petre, M. Moonen, M. Engels, B. Gyselinckx and H. Man, "Pilot-Aided Adaptive Chip Equalizer Receiver For Interference Suppression In DS-CDMA Forward Link", Proc. of VTC-Fall'2000, 2000.
DH	AF	Ying-Chang Liang, "A Code Division Multiple Access Downlink Receiver", U.S. Patent Application Serial No. 09/981,531, filed October 17, 2001 (57 p.).

EXAMINER

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DATE CONSIDERED

6-13-06

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.